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in this book internationally recognized researchers give a state of the art overview of the electronic device architectures required for the nano cmos era and beyond challenges relevant to the scaling of cmos nanoelectronics are addressed through different core cmos and memory device options in the first part of the book the second part reviews new device concepts for nanoelectronics beyond cmos the book covers the fundamental limits of core cmos improving scaling by the introduction of new materials or processes new architectures using soi multigates and multichannels and quantum computing

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